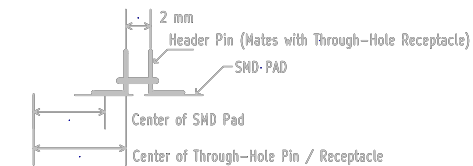


TOP SILK  
 TOP SOLDERMASK  
 TOP COPPER  
 BOTTOM COPPER  
 BOTTOM SOLDERMASK  
 BOTTOM SILK  
 PCB OUTLINE  
 DIMENSION / DRAWING LAYER

NOTES  
 MH = MOUNTING HOLE  
 \* P1-1, P2-1, P3-1 measured off center of surface mount pad, measured to center of through hole post, which corresponds to center of HDK J9, J10, J11 pin 1's



Dimensions in this drawing reference the center of the through-hole Pin, which is slightly off-center from the SMD pad center. However, these dimensions match the center of pin 1 of the through-hole 2mm receptacles on the HDK (J9, J10, J11)

Texas Instruments		Rev: *
File: HDK_EXPERIMENTER.brd		Id: 1/1
Sheet: 1/1		
Title: HDK Experimenter Board		
Size: B	Date: 11 nov 2011	
KICad E.D.A.	pcbnew (2011-07-08 BZR 3044)-stable	